

06502OMB No. 0651-0011

INFORMATION DISCLOSURE CITATION

Atty. Docket No.	06502-03-00000	Serial No.	09/504,876	RECEIVED SEP 10 2001 Technology Center 2100
Applicant	Paul Hinker et al.			
Filing Date	February 16, 2000	Group:	2782	

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
DT	5,742,793	4/1998	Sturges et al.	711	152	—
DT	5,864,867	1/1999	Krusche et al.	707	104.1	—

FOREIGN PATENT DOCUMENTS

	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
DT	EP 0 703 534 A1	3/1996	EPO	—	—	NO
DT	WO 99/10812 A1	3/1999	PCT	—	—	Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DT	Article "Caching Objects In A Data Space," <u>IBM Technical Disclosure Bulletin</u> , IBM Corp., Vol. 37, No. 10, October, 1994, pp. 587-590.

Examiner	Deusman	Date Considered	3/28/03
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
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OMB No. 0651-0011

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Atty. Docket No. 06502.0210-00000

Serial No. 504,876/09

Applicant: Paul J. HINKER et al.

Filing Date February 16, 2000

Group 2782 2186

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
DT	4,675,832	6/23/1987	Robinson et al.	345	441	
	5,325,533	6/28/1994	McInerney et al.	717	107	
	5,499,349	3/12/1996	Nikhil et al.	712	25	
	5,500,881	3/19/1996	Levin et al.	717	141	
	5,519,866	5/21/1996	Lawrence et al.	717	162	
	5,530,816	6/25/1996	Holt	712	47	
	5,613,063	3/18/1997	Eustace et al.	714	38	
	5,675,790	10/7/1997	Walls	709	205	
	5,675,802	10/7/1997	Allen et al.	717	103	
	5,689,712	11/18/1997	Heisch	717	130	
	5,748,961	5/5/1998	Hanna et al.	717	145	
	5,784,698	7/21/1998	Brady et al.	711	171	
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	5,850,554	12/15/1998	Carver	717	162	
	5,867,649	2/2/1999	Larson	709	201	
	5,872,977	2/16/1999	Thompson	717	122	
	5,905,488	5/18/1999	Demers et al.	345	173	
	5,963,975	10/5/1999	Boyle et al.	711	147	
	5,974,536	10/26/1999	Richardson	712	215	

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DT	5,978,892	11/2/1999	Noel et al.	711	170	
	5,991,893	11/23/1999	Snider	714	4	
	6,016,474	1/18/2000	Kim et al.	717	125	
	6,018,793	1/25/2000	Rao	711	130	
	6,044,438	3/28/2000	Olnowich	711	130	
	6,085,029	7/4/2000	Kolawa et al.	714	38	
	6,101,525	8/8/2000	Hecker	709	104	
DT	6,125,430	9/26/2000	Noel et al.	711	152	

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DT	"Algorithm Visualization System: Introduction," available online at: www.cp.eng.chula.ac.th/faculty/spj/research/avis/intro.html as of June 10, 1999, 2 pages
	"GeoMAMOS Project Home Page," available online at: www.ece.nwu.edu/~theory/geomamos.html as of June 10, 1999, 4 pages
	"Introduction," available online at: www.ece.nwu.edu/~theory/gstech_1.html/section_3_1.html as of June 10, 1999, 3 pages
DT	"Pentium Processor Family User Manual Volume 3: Architecture and Programming Manual," pp. 25-182, 25-183, 25-309, and 25-310, Intel Corp., (1994)

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PT	"Purify for Windows NT, Product Overview," Ver. 6.0, available online at: www.rational.com/products/purify_nt/prodinfo/index.jtmpl as of Jan 11, 1999, 3 pages
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	"Visualization for Developing Geometric Algorithms," available online at: www.ece.nwu.edu/~theory/gstech_1.html/section3_3.html as of June 10, 1999, 2 pages
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	Barry Wilkinson et al., "Parallel Programming," Prentice Hall, 1999
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	David E. Culler et al., "Parallel Computer Architecture", Morgan Kaufman Publishers, Inc., San Francisco, California, 1999
	Ian Foster, "Designing and Building Parallel Programs", Addison-Wesley Publishing Company, 1995
	Kevin Dowd and Charles R. Severance, "High Performance Computing," Second Edition, Chapter 10, Shared Memory Multiprocessors, August 1998, pp. 216-18
	Sun Microsystems Computer Company, "Prism 5.0 Reference Manual," Revision A., November 1997
	Sun Microsystems, "UltraSPARC User's Manual," UltraSPARC-1, UltraSPARC-II, July 1997, pp. 319-25
NT	Wasserman et al., "A Graphical, Extensible Integrated Environment for Software Development," Proceedings of the ACD SIGSOFT/SIGPLAN Software Engineering Symposium on Practical Software Development Environments, 1986, pp. 131-142

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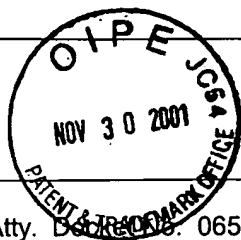
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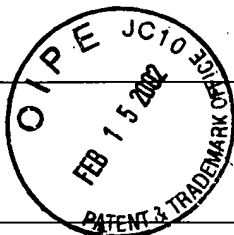
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Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
<i>NT</i>	5,553,235	9/3/1996	Chen et al.	<i>714</i>	<i>20</i>	
<i>I</i>	5,673,387	9/30/1997	Chen et al.	<i>714</i>	<i>38</i>	
<i>I</i>	5,805,795	9/8/1998	Whitten	<i>714</i>	<i>38</i>	
<i>NT</i>	6,269,457 B1	7/31/2001	Lane	<i>714</i>	<i>38</i>	

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Examiner Initial*	Document Number	Date	Country	Class	Sub Class	Translation Yes or No

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PT	4,812,996	3/14/1989	Stubbs	702	123	
I	5,079,707	1/7/1992	Bird et al.	701	35	
I	5,724,262	3/3/1998	Ghahramani	702	186	
HT	6,223,134 B1	4/24/2001	Rust et al.	702	121	PT

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U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
<i>PT</i>	5,325,499	6/28/1994	Kummer et al.	711	143	
	5,636,374 A	6/3/1997	Rodgers et al.	712	230	
	5,835,705	11/10/1998	Larsen et al.	714	47	
	6,052,708	4/18/2000	Flynn et al.	709	108	
	6,065,019	5/16/2000	Ault et al.	707	405	
	6,081,868	6/27/2000	Brooks	711	3	
<i>PT</i>	6,098,169	8/1/2000	Ranganathan	712	227	

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Examiner Initial*	Document Number	Date	Country	Class	Sub Class	Translation Yes or No
<i>PT</i>	DE 199 34 515 A1	1/27/2000				Yes (Abstract)
<i>PT</i>	EP 0 817 044 A2	1/7/1998				<i>NO</i>

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<i>PT</i>	Broberg et al., "Visualization and Performance Prediction of Multithreaded Solaris Programs by Tracing Kernel Threads", Department of Software Engineering and Computer Science, University of Karlskrona/Ronneby, Sweden, 7 pages
<i>PT</i>	Haggander et al., "Optimizing Dynamic Memory Management in a Multithreaded Application Executing on a Multiprocessor," International Conference on Parallel Processing, Minneapolis, MN, August 10-14, 1998, pp. 262-269

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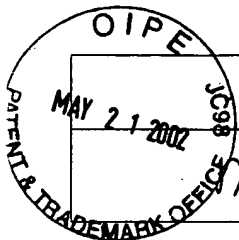
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Applicant: Paul HINKER et al.

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Filing Date: February 16, 2000	Group 2183 <i>2186</i>

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
<i>RT</i>	5,860,024	1/12/1999	Kyle et al.	<i>710</i>	<i>186</i>	<i>18 2002</i>
<i>J</i>	5,991,708	11/23/1999	Levine et al.	<i>702</i>	<i>186</i>	<i>18 2002</i>
<i>J</i>	6,009,514	12/28/1999	Henzinger et al.	<i>712</i>	<i>236</i>	<i>18 2002</i>
<i>RT</i>	6,205,537	3/20/2001	Albonesi	<i>712</i>	<i>236</i>	<i>18 2002</i>

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner <i>JRM</i>	Date Considered: <i>3/28/03</i>
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Group ~~2782~~ 2186

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
PT	5,075,847	12/24/1991	Fromme	717	136	—
	5,301,312	4/5/1994	Christopher, Jr. et al.	714	32	—
	5,353,401	10/4/1994	Iizawa et al.	345	763	—
	5,438,659	8/1/1995	Notess et al.	715	505	
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	5,745,897	4/28/1998	Perkins et al.	707	101	
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	6,311,320	10/30/2001	Jibbe	717	111	
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DT	EP 0 965 921 A2	12/22/1999		—	—	yes
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	FR 2 793 908 A1	11/24/2000		—	—	NO
	JP 03-282731 A	12/12/1991		—	—	NO
	JP 07-056716 A	3/3/1995		—	—	NO
	UK 2 343 029	4/26/2000		—	—	yes
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DT	Nathan P. Kropp, Philip J. Koopman, Daniel P. Siewiorek, "Automated Robustness Testing of Off-the-Shelf Software Components," Institute for Complex Engineered Systems, Carnegie Mellon University, Pittsburgh, PA, pp. 230-239, IEEE (1998)
DT	Valvano, "Debugging Strategies," Chapter 6, pp. 6.1 - 6.10, Real Time Debugging, Online Manual, June 7, 1999, Retrieved from the Internet: <URL:http://www.ece.utexas.edu/~valvano/EE360P/PDF/Ch6.pdf>

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Group 2183

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	Browne et al., "PAPI: Portable Interface to Hardware Performance Counters," Cewes Major Shared Resources Center Pet Technical Reports, Vol. 99-06, 1999, Online, <URL:http://www.wes.hpc.mil/pet/tech_reports/reports/pdf/tr_9906.pdf>, pp. 3-13
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MT	Zagha et al., "Performance Analysis Using the MIPS R10000 Performance Counters", SC96 Technical Papers, Online, <URL:http://www.supercomp.org/sc96/proceedings/SC96PROC/ZAGHA/INDEX.HTM>, November 1996, pp. 1-22

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